

1                   **TITLE OF THE INVENTION**

2                   **HOT-PLUGGING METHOD OF DISPLAY APPARATUS**

3                   **CLAIM OF PRIORITY**

4                   This application makes reference to, incorporates the same herein, and claims all benefits  
5                   accruing under 35 U.S.C. §119 from an application entitled *Hot-Plugging Method of Display*  
6                   *Apparatus* earlier filed in the Korean Industrial Property Office on 25 November 1997, and there  
7                   duly assigned Serial No. P97-62917 by that Office.

8                   **BACKGROUND OF THE INVENTION**

9                   **Technical Field**

10                  The present invention relates to a method of recognizing whether a display apparatus is  
11                  newly connected to a main body of a computer, and more particularly to a hot-plugging method for  
12                  display apparatus which is capable of judging whether a display apparatus is replaced with another  
13                  by polling or interrupting.

14                  **Related Art**

15                  Computer systems are information handling systems that are utilized by many individuals  
16                  and businesses today. A computer system can be defined as a microcomputer that includes a central

1 processing unit (CPU), a volatile memory, a non-volatile memory such as read only memory (ROM),  
2 a display monitor, a keyboard, a mouse or other input device such as a trackball, a floppy diskette  
3 drive, a compact disc-read only memory (CD-ROM) drive, a modem, a hard disk storage device, and  
4 a printer. A computer system's main board, which is a printed circuit board known as a  
5 motherboard, is used to electrically connect these components together.

6 The central processing unit is often described as a microprocessor. The microprocessor is  
7 an electronic component having internal logic circuitry handling most, if not all, the data processing  
8 in the computer system. The internal logic circuitry of microprocessors is typically divided into  
9 three functional parts known as the input/output (I/O) unit, the control unit, and the arithmetic-logic  
10 unit (ALU). These three functional parts interact together and determine the power and performance  
11 of the microprocessor. The combination of the control unit and the arithmetic-logic unit can be  
12 referred to as the central processing unit. Also, the combination of the input/output unit, the control  
13 unit, and the arithmetic-logic unit can be referred to as the central processing unit.

14 One example of non-volatile memory is read only memory (ROM). Information stored in  
15 non-volatile memory can remain unchanged even when there is a power failure. The information  
16 stored in non-volatile memory will stay there until it is changed. Read only memory (ROM) is used  
17 to store important information such as instructions for the central processing unit (CPU). There are  
18 different types of read only memory (ROM) including electrically-erasable-programmable-read-  
19 only-memory (EEPROM) chip and flash-read-only-memory (flash-ROM). The flash-ROM can also

1 be referred to as flash memory.

2 Computer systems include a basic input output system (BIOS) which is an especially  
3 important program stored in read only memory (ROM). The basic input output system (BIOS) tests  
4 a computer every time the computer is powered on. The basic input output system (BIOS) can  
5 allocate a computer system's resources automatically, making adjustments needed to accommodate  
6 new hardware. Also, the basic input output system (BIOS) governs how system board components  
7 interact.

8 When the computer system is powered on, the basic input output system (BIOS) immediately  
9 takes control of the computer system and its components. The first duty of the basic input output  
10 system (BIOS) is to perform a series of diagnostic routines called the power on self test (POST)  
11 routine, which ensures that every part of the computer system's hardware is functioning properly.

12 Consider a booting operation for a computer system using Microsoft Windows 95 as the  
13 operating system. First, a user starts the booting process by turning on a power switch to supply  
14 power to the computer system. Next, a basic input output system (BIOS) executes a power on self  
15 test (POST) to test and initialize the computer system's components. The basic input output system  
16 is software embedded on an integrated circuit located on the computer system's main board. The  
17 basic input output system also handles low-level input/output to various peripheral devices  
18 connected to the computer system. The power on self test is the part of the basic input output system

1 that takes control immediately after the computer is turned on. The power on self test initializes the  
2 computer hardware so that an operating system can be booted (loaded). When the power on self test  
3 procedure is finished, a plug and play operation is executed if it is determined that all hardware is  
4 operating normally. A specification prepared by Microsoft and Intel that presents a mechanism to  
5 provide automatic configuration capability to Industry Standard Architecture (ISA) cards thus  
6 enabling full Plug and Play in the computer is dated 5 May 1994 and is entitled, "Plug and Play ISA  
7 Specification, Version 1.0a." During the plug and play operation, the entire computer system is  
8 scanned for new hardware.

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16 The plug and play operation is executed every time that the computer system is booted. Now  
the plug and play operation shall be described in detail. At first, a configuration manager, which  
manages the plug and play operation, checks to determine whether a bus is being used. That is, a  
bus enumerator, which is a special driver managing a bus, builds a database by gathering information  
on peripheral devices. Next, the configuration manager uses the database built by the bus  
enumerator to build a hardware tree structure and stores the hardware tree structure in a memory.  
The hardware tree structure indicates the composition of the hardware being used in the computer  
system.

17 Subsequently, the configuration manager operates drivers of each peripheral device indicated  
18 in the hardware tree structure. After the drivers of each peripheral device are operated, a resource  
19 arbitrator, which resolves conflicts among system resources, distributes system resources to each

1 peripheral device. Here, system resources include interrupt request lines (IRQs), input/output port  
2 addresses, memory addresses, direct memory access (DMA) channels, and more.

3 Lastly, the configuration manager transmits information to the bus enumerator regarding  
4 details of the distribution of the system resources. The bus enumerator transmits information  
5 regarding allowed system resources to each peripheral device connected to the bus, thereby enabling  
6 the user to utilize the peripheral devices.

7 Next, when the execution of the plug and play operation is completed, system files used by  
8 the Microsoft Windows 95 operating system are executed to complete the booting process of the  
9 computer system. Then the booting process of the computer system is complete.

10 I believe that the plug and play operation is only performed when a computer system is  
11 booted. Therefore, I have found that it can be difficult or impossible to disconnect a first display  
12 from a computer after the computer has been booted and while the computer is operating, to connect  
13 a second display to the computer, and then to use the second display with the computer, all without  
14 rebooting the computer.

## 15 SUMMARY OF THE INVENTION

16 An object of the present invention is to provide a hot-plugging method of display apparatus  
17 in which a main body of a computer automatically recognizes a newly connected display apparatus

1 so as to normally operate it without re-driving the main body of the computer even though the  
2 display apparatus is replaced while the main body of the computer is being operated.

3 To achieve the above objects, there is provided a hot-plugging method of display apparatus  
4 including the steps of reading information on a display apparatus by a predetermined data  
5 communication upon recognizing that a display apparatus is newly connected while a controller  
6 provided in a main body of a computer maintains judging of a new connection of display apparatus;  
7 judging whether the read information is identical to the information stored at a memory on the  
8 current display apparatus; and storing the newly read information if they are not identical to each  
9 other, determining an optimal resolution corresponding to the newly connected display apparatus  
10 and transmitting it to a video card.

11 To achieve these and other objects in accordance with the principles of the present invention,  
12 as embodied and broadly described, the present invention provides a method, comprising: while  
13 power is being supplied to a processing unit, detecting whether a video display unit is newly coupled  
14 to a connecting unit of said processing unit, said video display unit conveying varying visual  
15 information to a user, and said processing unit processing data including the varying visual  
16 information; when said video display unit is detected as being newly coupled to said connecting unit  
17 while power is being supplied to said processing unit, reading first data corresponding to said video  
18 display unit; determining whether said first data corresponds to second data stored in a memory unit;  
19 and when said first data does not correspond to said second data stored in said memory unit, storing

1 said first data in said memory unit and determining a resolution corresponding to said video display  
2 unit and transmitting said resolution to a video card coupled to said video display unit.

3 To achieve these and other objects in accordance with the principles of the present invention,  
4 as embodied and broadly described, the present invention provides an apparatus, comprising: a video  
5 display unit conveying varying visual information to a user; and a processing unit processing data  
6 including the visual information, said processing unit detecting whether said video display unit is  
7 newly coupled to said processing unit while power is being supplied to said processing unit, said  
8 processing unit reading first data corresponding to said video display unit when said video display  
9 unit is detected as being newly coupled to said processing unit while power is being supplied to said  
10 processing unit, said processing unit determining whether said first data corresponds to second data  
11 stored at said processing unit, wherein said processing unit stores said first data and determines  
12 resolution data corresponding to said video display unit and transmits said resolution data when said  
13 first data does not correspond to said second data.

14                  To achieve these and other objects in accordance with the principles of the present invention,  
15 as embodied and broadly described, the present invention provides a method, comprising: while  
16 power is being supplied to a processing unit, detecting whether a video display unit is newly coupled  
17 to said processing unit, said video display unit conveying varying visual information to a user, and  
18 said processing unit processing data including the visual information; when said video display unit  
19 is detected as being newly coupled to said processing unit while power is being supplied to said

1 processing unit, reading first data corresponding to said video display unit; determining whether said  
2 first data corresponds to second data stored in a memory unit; and when said first data does not  
3 correspond to said second data stored in said memory unit, storing said first data in said memory unit  
4 and determining resolution data corresponding to said video display unit and transmitting said  
5 resolution data to a first device.

6 The present invention is more specifically described in the following paragraphs by reference  
7 to the drawings attached only by way of example. Other advantages and features will become  
8 apparent from the following description and from the claims.

#### 9                   **BRIEF DESCRIPTION OF THE DRAWINGS**

10                  In the accompanying drawings, which are incorporated in and constitute a part of this  
11 specification, embodiments of the invention are illustrated, which, together with a general  
12 description of the invention given above, and the detailed description given below serve to exemplify  
13 the principles of this invention.

14                  FIG. 1 is a block diagram showing a signal connection between a display apparatus and a  
15 main body of a computer including a digital data communication (DDC) function;

16                  FIG. 2 is a flowchart showing an embodiment of a plug-and-play operation of the display  
17 apparatus connected to the main body of the computer of FIG. 1;

18                  FIG. 3 is a flowchart showing a first embodiment of hot-plugging of the display apparatus,  
19 in accordance with the principles of the present invention;

1 FIG. 4 is a flowchart showing a second embodiment for judging whether the display  
2 apparatus is connected, in accordance with the principles of the present invention; and

3 FIG. 5 is a block diagram showing a signal connection between the display apparatus and the  
4 main body of a computer including a communication using a universal serial bus (USB), in  
5 accordance with the principles of the present invention.

6 **DETAILED DESCRIPTION OF THE INVENTION**

7 While the present invention will be described more fully hereinafter with reference to the  
8 accompanying drawings, in which a preferred embodiment of the present invention is shown, it is  
9 to be understood at the outset of the description which follows that persons of skill in the appropriate  
10 arts may modify the invention here described while still achieving the favorable results of this  
11 invention. Accordingly, the description which follows is to be understood as being a broad, teaching  
12 disclosure directed to persons of skill in the appropriate arts, and not as limiting upon the present  
13 invention.

14 Turn now to FIG. 1, which is a block diagram showing a signal connection between a display  
15 apparatus and a main body of a computer including a digital data communication (DDC) function.  
16 FIG. 1 includes a main body 100 of a computer and a display apparatus 200. The main body 100  
17 of the computer includes a memory 130 for storing software or various data required for proper  
18 operation of the main body 100; a digital data communication (DDC) interface 110 for being  
19 controlled by a controller 120 and for serving as a signal-interface for digital data communication;

1 and a video card 140 for converting a video signal provided from the controller 120 to a transmit  
2 signal and transmitting it to the display apparatus 200.

3       The display apparatus 200 includes a video signal processing unit 220 for amplifying and  
4 outputting the video signal transmitted from the video card 140 of the main body of the computer  
5 100 so as to display it on a screen, and a digital data communication (DDC) device 210 for storing  
6 information on the display apparatus 200 and transmitting it to the main body 100 of the computer  
7 through digital data communication.

8       Regarding FIG. 1, an operation of the block diagram of the signal connection between the  
9 display apparatus 200 and the main body 100 of the computer will now be described. When the  
10 display apparatus 200 is connected to the main body 100 of the computer by digital data  
11 communication, the digital data communication device 210 of the display apparatus 200 is actuated,  
12 and the information stored at the digital data communication device 210 is transmitted to the  
13 controller 120 through the digital data communication interface 110 of the main body 100 of the  
14 computer.

15       Upon receipt of the information on the display apparatus 200, the controller 120 reads to  
16 compare it with the information on the existing display apparatus as stored at the memory 130, and  
17 if they are not the same, the controller stores the newly input information instead of the existing  
18 information. And the controller 120 reads the resolution outputted to the video card 140 as a value

1 corresponding to the newly connected display apparatus from the memory 130 for replacement. The  
2 video card 140 generates a horizontal and a vertical synchronous signals to be suitable to the  
3 information provided from the controller 120 and converts a video signal such as color signal into  
4 a suitable type for transmission.

5 The signal transmission from the main body 100 of computer to the display apparatus 200  
6 can be either an analog signal or a digital signal type. Meanwhile, the video signal processing unit  
7 220 of the display apparatus 200 converts the synchronous signal and the video signal transmitted  
8 from the video card 140 into a signal level suitable to the display apparatus 200, which is then  
9 amplified so as to display it on the screen of the display apparatus 200, and thereafter outputs it to  
10 the display apparatus 200 such as a cathode-ray tube or a liquid crystal display device.

11 Turn now to FIG. 2, which is a flowchart showing an embodiment of a plug-and-play  
12 operation of the display apparatus connected to the main body of the computer of FIG. 1. At step  
13 S210, in order to perform a plug-and-play operation of the display apparatus 200 of FIG. 1, power  
14 is supplied to the main body 100 of the computer of FIG. 1. At step S220, a determination is made  
15 as to whether the display apparatus 200 is connected to the main body 100 of computer. After step  
16 S220, the step S230 is performed only if the display apparatus 200 is connected. After step S220,  
17 the process ends if the display apparatus 200 is not connected. At step S230, information pertaining  
18 to the display apparatus 200 is read from the digital data communication device 210 if the display  
19 apparatus 200 is connected. At step S240, resolution data corresponding to the information read

1 from the digital data communication device 210 is transmitted to the video card 140 of FIG. 1. The  
2 above steps are controlled by the controller 120 of the main body 100 of computer. More detailed  
3 description follows pertaining to FIG. 2.

4 In FIG. 2, at step S210, power is supplied to the main body 100 of computer. At step S220,  
5 the controller 120 checks each port where peripheral equipment is connected to judge where they  
6 are connected to each port. For example, a voltage of a predetermined pin of a port where the  
7 display apparatus 200 is to be connected is checked, and then its connection is judged according to  
8 the size of the voltage.

9 In FIG. 2, at step S231, when the display apparatus 200 is connected to the main body 100  
10 of the computer through digital data communication, the digital data communication device 210 of  
11 the display apparatus 200 is accessed. At step S232, various information for the display apparatus  
12 200 is read through the digital data communication interface 110.

13 The digital data communication device 210 is a semiconductor device which stores  
14 information such as a product company and a size of the display apparatus where it is inserted.  
15 Reading information as stored at the digital data communication device is only possible through this  
16 digital data communication device 210.

17 At step S240, after reading the information of the display apparatus 200 from the digital data

1 communication device 210, the controller 120 controls a transmitting of the resolution corresponding  
2 to the information as read to the video card 140. At step S241, the transmitting of the resolution is  
3 performed in such a way that first read information is stored at the memory 130, and then, at step  
4 S242, the optimal resolution corresponding to the connected display apparatus 200 is determined and  
5 then is read from the memory 130. At step S243, the optimal resolution corresponding to the  
6 connected display apparatus 200 is transmitted to the video card 140.

7 However, as to the plug-and-play function between the main body 100 of computer and the  
8 display apparatus 200 controlled as described above, the main body 100 of the computer recognizes  
9 the display apparatus 200 only when the power is initially applied to the main body 100 of the  
10 computer so that the main body 100 of the computer can be initialized, which causes a problem in  
11 case that the display apparatus 200 is replaced while the main body 100 of the computer is being  
12 operated, in that the display apparatus 200 cannot be initialized. That is, plug-and-play function can  
13 be performed, while the hot-plugging function is not available.

14 A preferred embodiment of the present invention will now be described with reference to the  
15 accompanying drawings. Turn now to FIG. 3, which is a flowchart showing a first embodiment of  
16 hot-plugging of the display apparatus, in accordance with the principles of the present invention.  
17 As shown in FIG. 3, the hot-plugging method of a display apparatus includes the following steps.  
18 At step S310, a determination is made as to whether a new display apparatus is connected while a  
19 main body of a computer is being driven. At step S320, reading information on the display

1 apparatus is read by means of a predetermined data communication in case that the display apparatus  
2 is newly connected to the main body of the computer. At step S330, a determination is made as to  
3 whether the read information is identical to the existing information as stored at a memory. At step  
4 S340, storing the read information to the memory if the two information are not identical to each  
5 other according to the determination of step S330, determining an optimal resolution corresponding  
6 to the newly connected display apparatus, and transmitting the optimal resolution to a video card,  
7 which is performed by a controller 120 (as shown in FIG. 1). More details regarding FIG. 3 are  
8 provided below.

9 In FIG. 3, at step S310, it is judged whether the display apparatus 200 is newly connected  
10 by replacement while the main body 100 of the computer is being driven in a way that a voltage  
11 variation of a predetermined pin of a port for digital data communication is checked by successively  
12 polling a digital data communication interface 110 in a constant time interval. Therefore, at step  
13 S311, a successive polling of a digital data communication interface unit 110 is performed. Then  
14 at step S312, a determination is made as to whether a new display apparatus has been connected.  
15 At this time, the judgement of whether the display apparatus is connected or not is made not only  
16 while the main body 100 of the computer is being driven but when a power is applied to the main  
17 body of the computer.

18 At step S320, when the display apparatus 200 is newly connected to the main body of the  
19 computer, a step for reading information on the display apparatus 200 by means of a predetermined

1 data communication is performed in a way that the digital data communication device 210 is first  
2 accessed and then the information is read through the digital data communication interface 110.  
3 Therefore, at step S321, a digital data communication device 210 is accessed. Then at step S322,  
4 information pertaining to the newly attached display apparatus is read from the digital data  
5 communication device 210.

6 In FIG. 3 at step S330, upon reading the information of the display apparatus 200 from the  
7 digital data communication device 210, the newly read information and the information as  
8 previously stored at the memory 130 are compared so as to determine whether they are the same.  
9 At step S341, if the newly read information and the previously stored information are deemed to be  
10 not the same, then the newly read information is stored at the memory 130. At step S342, the  
11 optimal resolution corresponding to the new display apparatus 200 is determined and read from the  
12 memory 130. At step S343, the optimal resolution is transmitted to the video card 140.

13 Turn now to FIG. 4, which is a flowchart showing a second embodiment for judging whether  
14 the display apparatus is connected, in accordance with the principles of the present invention. It is  
15 helpful to refer to FIGS. 3 and 4 together. In the control process as shown in FIG. 3, the judgement  
16 as to whether or not the display apparatus 200 is connected to the main body 100 of the computer  
17 is made in a way that a voltage variation of a predetermined pin of a port for the digital data  
18 communication is checked by repeatedly polling the digital data communication interface 110 in a  
19 constant time interval. This control process was previously described during the discussion of steps

1 S310, S311, and S312 above.

2        However, step S410 comprises an alternative to steps S310, S311, and S312. Thus, in FIG.  
3        4 at step S410, there can be used a method for checking whether an interrupt signal is generated and  
4        inputted from the digital data communication interface 110 to the controller 120.

5        An example of step S410 in FIG. 4 is as follows. When the voltage of the predetermined pin  
6        of the port for digital data communication is varied, which is interpreted as a new display apparatus  
7        200 being newly connected to the main body 100 of the computer, then the digital data  
8        communication interface 110 outputs a predetermined interrupt signal to the controller 120.

9        While the display apparatus is performing the hot-plugging, when the display apparatus 200  
10      is newly connected to the main body of the computer, a predetermined data communication, that is,  
11      a communication means, for reading information on the display apparatus 200 may be any other  
12      communication means for use including a radio communication such as infrared if they are able to  
13      transmit data besides the digital data communication as shown in FIG. 1. Thus, infrared signals and  
14      other types of signals may be used for the communication among the devices depicted in FIG. 1.

15      Turn now to FIG. 5, which is a block diagram showing a signal connection between the  
16      display apparatus and the main body of a computer including a communication using a universal  
17      serial bus (USB), in accordance with the principles of the present invention. FIG. 5 does not include

1 the digital data communication device 210 and the digital data communication interface 110 depicted  
2 in FIG. 1 for performing digital data communication. Instead of the DDC device 210 and the DDC  
3 interface 110, the main body 100 of the computer of FIG. 5 includes a root hub 150 in the main body  
4 100 of the computer, along with a hub 230 and a second memory 250 in the display apparatus 200,  
5 in order to perform communication using universal serial bus (USB).

6 In FIG. 5, the second memory 250 is a device for storing information pertaining to the  
7 display apparatus 200. Thus, second memory 250 in FIG. 5 performs a function similar to the  
8 function performed by the digital data communication device 210 in FIG. 1.

9 In FIG. 5, the root hub 150 and the hub 230 are elements essentially provided to the main  
10 body 100 of the computer and the display apparatus 200 for the communication using universal  
11 serial bus, thus performing effectively the same functions as the interface which enables the  
12 communication between instruments. The root hub 150 serves to control the hub 230 during  
13 universal serial bus communications.

14 In FIG. 5, the microcomputer 240 wholly controls operations of each constructive element  
15 as described above. Namely, when the display apparatus 200 is connected to the main body 100 of  
16 the computer, the microcomputer 240 reads the information on the display apparatus 200 from the  
17 second memory 250 and transmits it to the controller 120 in the main body 100 of the computer  
18 through the hub 230 and to the root hub 150 via the universal serial bus communications cable

1 coupling the hub 230 and the root hub 150.

2 An overview of the universal serial bus is shown below. A computer system has peripheral  
3 devices connected, such as a monitor and keyboard. A user might want to use additional peripheral  
4 devices, such as a mouse, modem, printer, and scanner. When a user tries to use multiple peripheral  
5 devices with a computer, it can be difficult to connect them since each peripheral typically has a  
6 unique type of connector. The user must carefully match the plug from each peripheral with a  
7 corresponding connector on the computer due to all the different connector types. Also, it can be  
8 difficult to configure the computer to communicate with all the peripheral devices due to the fact that  
9 some peripheral devices require unique types of hardware and software. To solve the above  
10 described problems, a universal serial bus (USB) system has been developed. The universal serial  
11 bus is a basic system for connecting peripheral devices to a computer. Peripheral devices connected  
12 to a universal serial bus system are also referred to as universal serial bus peripheral devices. All  
13 USB devices are compatible with standard USB cables and standard USB connectors in order to  
14 simplify the process of connecting devices to a computer system. A keyboard or monitor can be  
15 directly connected to a computer or a universal serial bus within the computer. Other peripheral  
16 devices can be easily connected to the computer with the use of an expanded hub built into the  
17 keyboard or monitor, or even via an independent universal serial bus. The expanded hub offers  
18 additional connection sockets, and can be connected in a hierarchical tree form. Peripheral devices  
19 may be located close to each other or can be located several meters from each other, with the use of  
20 a universal serial bus hub. A universal serial bus is able to connect a total of 127 USB devices to

1 one computer. The operating voltage transmitted through the universal serial bus is limited to 5  
2 volts. Thus, peripherals connected on a universal serial bus are limited in the amount of power they  
3 can consume. A rapid data transmission rate of 12 megabits per second on the universal serial bus  
4 is one of the advantageous features of the universal serial bus. The major advantages of the universal  
5 serial bus include the simplicity and convenience of attaching and detaching peripheral devices to  
6 the computer. The universal serial bus detects whether a device is added or removed when related  
7 information is offered from a computer. The USB devices can be added to a computer system or  
8 removed from a computer system while the computer system is on, unlike non-USB configurations,  
thus eliminating the need for a reboot of the computer system.

9  
10 The display apparatus 200 depicted in FIGS. 1 and 5 can be a cathode ray tube, a liquid  
11 crystal display, a gas-plasma display, a light emitting diode display, an electro-luminescent display,  
12 a field emission display, or any other type of video display.

13 As described above, according to hot-plugging method of display apparatus, in case of  
14 replacing the display apparatus 200 to be connected to the main body 100 of the computer, the main  
15 body 100 of the computer automatically recognizes the newly connected display apparatus 200 and  
16 provides an initializing method, so that the newly connected display apparatus 200 can be normally  
17 operated without re-operating the main body 100 of the computer. Thus, the main body 100 of the  
18 computer does not need to be rebooted in order to attach a new monitor.

1       The foregoing paragraphs describe a method for recognizing whether a display apparatus is  
2       newly connected to a main body of a computer, and more particularly, a hot-plugging method for  
3       display apparatus which is capable of judging whether a display apparatus is replaced with another  
4       by polling or interrupting, and if it is replaced, reading data of the replaced display apparatus by  
5       means of a predetermined data communication, and initializing the main body of the computer to  
6       fit the replaced display apparatus.

7       While the present invention has been illustrated by the description of embodiments thereof,  
8       and while the embodiments have been described in considerable detail, it is not the intention of the  
9       applicant to restrict or in any way limit the scope of the appended claims to such detail. Additional  
10      advantages and modifications will readily appear to those skilled in the art. Therefore, the invention  
11      in its broader aspects is not limited to the specific details, representative apparatus and method, and  
12      illustrative examples shown and described. Accordingly, departures may be made from such details  
13      without departing from the spirit or scope of the applicant's general inventive concept.